



Substitute for Form 149
INFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(use as many sheets as necessary)

Sheet

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of

6

Complete if Known

Application Number	09/981,620
Filing Date	October 16, 2001
First Named Inventor	Richard L. Coulson
Art Unit	2189
Examiner Name	K. Verbrugge
Attorney Docket Number	42390P11456

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number Number - Kind Code ² (if known)	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
KV		US-4,908,793	03-13-1990	Yamagata, et al.	
KV		US-4,972,364	11-20-1990	Barrett, et al	
KV		US-5,046,043	09-03-1991	Miller, et al	
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KV		US-6,052,789	04-18-2000	Lin	

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Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code - Number - Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ³
KV		EP 0 210 384	02-24-1987	HEWLETT PACKARD		
KV		GB 2 210 480	06-07-1989	Sun Microsystems		
KV		WO 93/21579	10-28-1993	Storage Technology		
KV		JP 6-236241	08-23-1994	Sharp		
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KV		WO 02/01365 A3	01-03-2002	Intel		
KV		WO 03/003202 A1	01-09-2003	Intel		
KV		WO 03/003217 A2	01-09-2003	Intel		

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Substitute for Form 1449A/PTO & TRADEMARK INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Complete if Known	
Sheet	2	of	6
		Application Number	09/981,620
		Filing Date	October 16, 2001
		First Named Inventor	Richard L. Coulson
		Art Unit	2189
		Examiner Name	K. Verbrugge
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KV	US-6055180	04-25-2000	Gudesen et al	
KV	US-6064615	05-16-2000	Gudesen	
KV	US-6081883	06-27-2000	Popelka et al	
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KV	US-6122711	09-19-2000	Mackenthun et al	
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KV	US-6725342	04-20-2004	Coulson	
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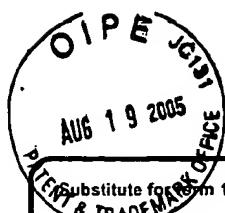
Examiner Signature	Kevi Verlage	Date Considered	9/5/05
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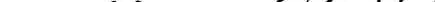
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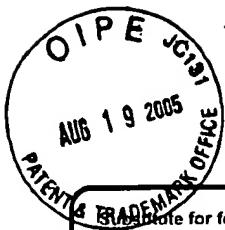
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TRADEMAKES for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet **4** of **6**

Complete if Known

Application Number	09/981,620
Filing Date	October 16, 2001
First Named Inventor	Richard L. Coulson
Art Unit	2189
Examiner Name	K. Verbrugge
Attorney Docket Number	42390P11456

NON PATENT LITERATURE DOCUMENTS

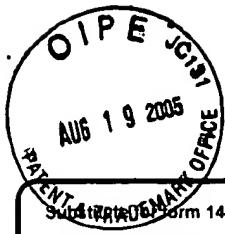
Examiner Initials*	Cite No.*	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>KV</i>		Auciello, Orlando, et al., "The Physics of Ferroelectric Memories", Physics Today, pp. 22-27 (Jul. 1998).	
<i>KV</i>		Date, M., et al., "Opto-Ferroelectric Memories Using Vinylidene Fluoride Copolymers", IEEE, pp. 298-302 (1988).	
<i>KV</i>		Hodges, David A., et al., "Analysis and Design of Digital Integrated Circuits", McGraw-Hill, Inc., pp. 388-389 (1983).	
<i>KV</i>		Derwent Abstract, Derwent Accession No. 2000-348269, Patent Publication No. RD 431135 A, 3 pages (March 10, 2000).	
<i>KV</i>		IBM Technical Disclosure Bulletin, "Non-Volatile Cache Storage Allocation Algorithm", Vol. 38, No. 12, pp. 39-41 (Dec. 1995).	
<i>KV</i>		Lang, Sidney B., et al., "Pyroelectric Applications of Ferroelectric Polymers", IEEE, pp. 251-255 (1988).	
<i>KV</i>		Merz, W. J., et al., "Ferroelectric Storage Devices", Bell Laboratories Record, pp. 335-337, 339-342 (Sep. 1955).	

Examiner Signature	<i>Kevin Verbrugge</i>	Date Considered	9/5/05
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Substitute Form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet 5 of 4

Complete if Known

Application Number	09/981,620
Filing Date	October 16, 2001
First Named Inventor	Richard L. Coulson
Art Unit	2189
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Attorney Docket Number	42390P11456

NON PATENT LITERATURE DOCUMENTS

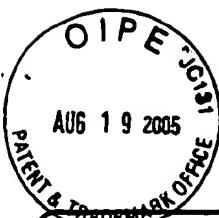
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
KV		Moazzami, Reza, et al., "A Ferroelectric DRAM Cell for High-Density NVRAM's", IEEE Electron Device Letters, Vol. 11, No. 10, pp. 454-456 (Oct. 1990).	
PW		Pulvari, C.F., "Ferroelectrics and their Memory Applications", IRE Transactions - Component Parts, pp. 3-11 (Mar. 1956).	
PW		Ramtron, FM1608 Preliminary Datasheet, Ramtron International Corporation, Colorado Springs, Colorado, pp. 1-12 (Dec. 7, 1999).	
PW		Ramtron, FM24C64 Preliminary Datasheet, Ramtron International Corporation, Colorado Springs, Colorado, pp. 1-13 (Dec. 7, 1999).	
PW		Ramtron, FRAM Technology, Ramtron International Corporation, Colorado Springs, Colorado, 2 pages (Jan. 1994).	
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KV		Robertson, Jack, "Hyundai Enters Japanese-dominated FeRAM Market", Silicon Strategies, A Service of Semiconductor Business News, CMP Media Inc., www.siliconstrategies.com (Sep. 3, 1998).	

Examiner Signature	Kevi Verbrugge	Date Considered	9/5/05
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Sheet 6 of 6

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First Named Inventor	Richard L. Coulson
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W		U.S. Patent Application No. 09/602,008, entitled "In-Line Cache", by Richard Coulson, filed June 23, 2000.	
W		U.S. Patent Application No. 09/602,009, entitled "Non-Volatile Cache Expansion Board", by Richard Coulson, filed June 23, 2000.	
W		U.S. Patent Application No. 09/602,010, entitled "Non-Volatile Cache", by Richard Coulson, filed June 23, 2000.	
W		U.S. Patent Application No. 09/602,011, entitled "Non-Volatile Cache Integrated with Mass Storage Device", by Richard Coulson, filed June 23, 2000.	
W		White, Ron, "How Computers Work", Ziff-Davis Press, pp. 71-73 (1993).	

Examiner Signature	<i>Kevin Verbrugge</i>	Date Considered	9/5/05
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